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8

Re Applic of	Raminderpal Singh et al.
Docket No.	FIS9-2004-0073-US1
Serial No.	10/807,478
Filing Date	03/23/2004
Attorney	H. Daniel Schnurmann

Attached: Amendment Transmittal Letter & Amendment

PLEASE DELIVER TO:  
EXAMINER: Stacy Whitmore  
ART UNIT: 2825  
PHONE NO: 571-272-1685  
FAX NO: 571-273-83008

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## AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Raminderpal Singh et al.

Docket No.

FIS9-2004-0073-US1

Application No.  
10/807,478Filing Date  
03/23/2004Examiner  
Stacy WhitmoreCustomer No.  
32074Group Art Unit  
2825Confirmation No.  
3056

Invention: Method of Checking the Layout Versus the Schematic of Multi-Fingered MOS Transistor Layouts Using a Sub-Circuit Based Extraction

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## COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

## CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	17	20 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	3	3 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

No additional fee is required for amendment.

Please charge Deposit Account No. in the amount of

A check in the amount of to cover the filing fee is enclosed.

The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0458

Any additional filing fees required under 37 C.F.R. 1.16.

Any patent application processing fees under 37 CFR 1.17.

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Signature

Dated: January 17, 2006

H. Daniel Schnurmann  
Reg. No.35,791  
Telephone No. 845-894-2481

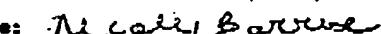
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Nicole Barrese

Nicole Barrese  
Signature of Person Transmitting Paper and Fee

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Raminderpal Singh, et al.	Date: January 17, 2006
Serial Number: 10/807,478	Examiner: Stacy Whitmore
Filed: 3/23/2004	Group Art Unit: 2825
Title: Method of Checking the Layout Versus the Schematic of Multi-Fingered MOS Transistor Layouts Using a Sub-Circuit Based Extraction	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

## AMENDMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated October 31, 2005, the following Amendment is being submitted to place the above-identified application in *prima facie* condition for allowance.

Kindly consider the following Remarks:

FIS9-2004-0073-US1

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PAGE 3/9 \* RCVD AT 1/17/2006 10:54:02 AM [Eastern Standard Time] \* SVR:USPTO-EFXRF-6/28 \* DNIS:2738300 \* CSID:845 892 6363 \* DURATION (mm:ss):02:32